

FIG 2 PRIOR ART

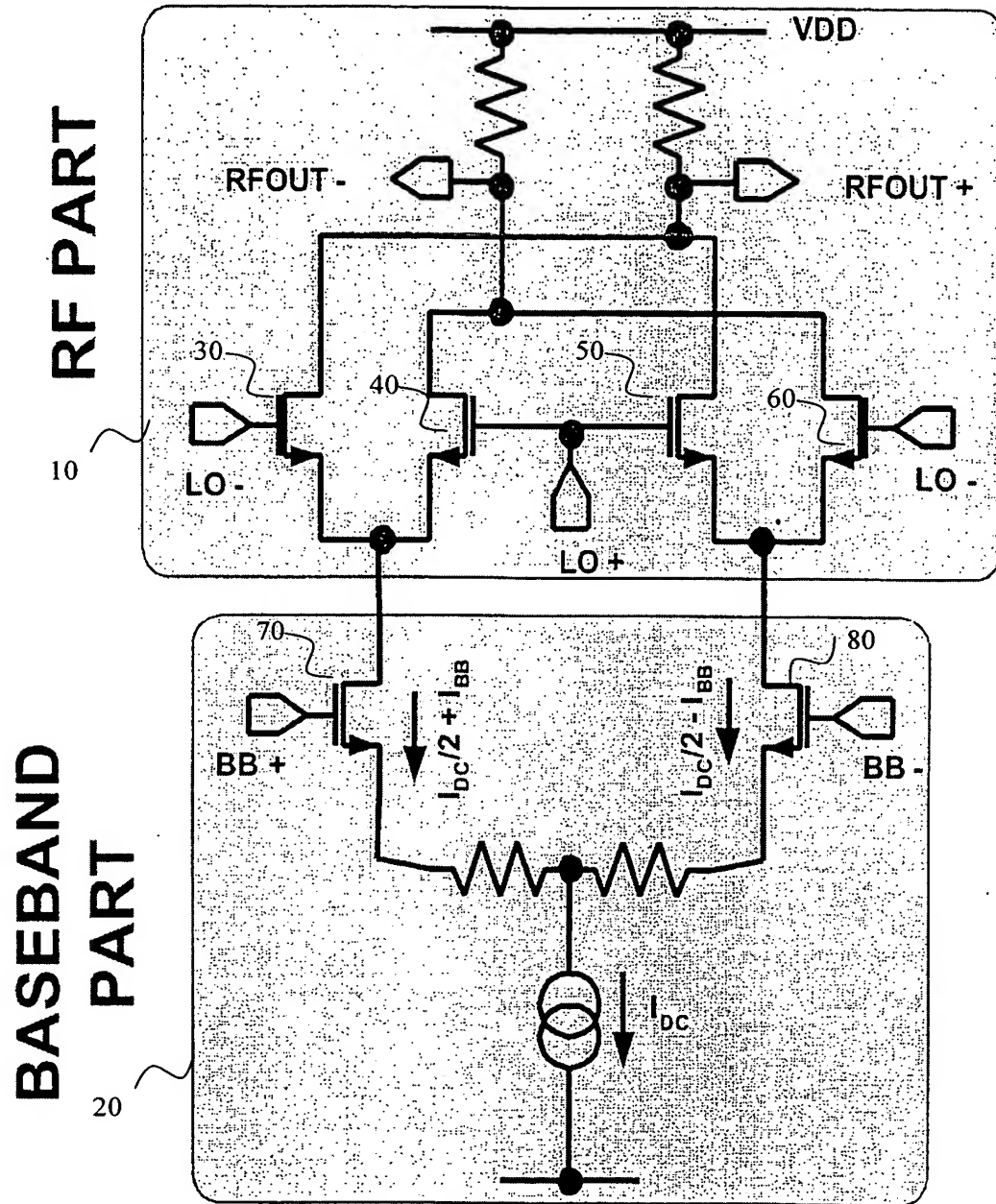
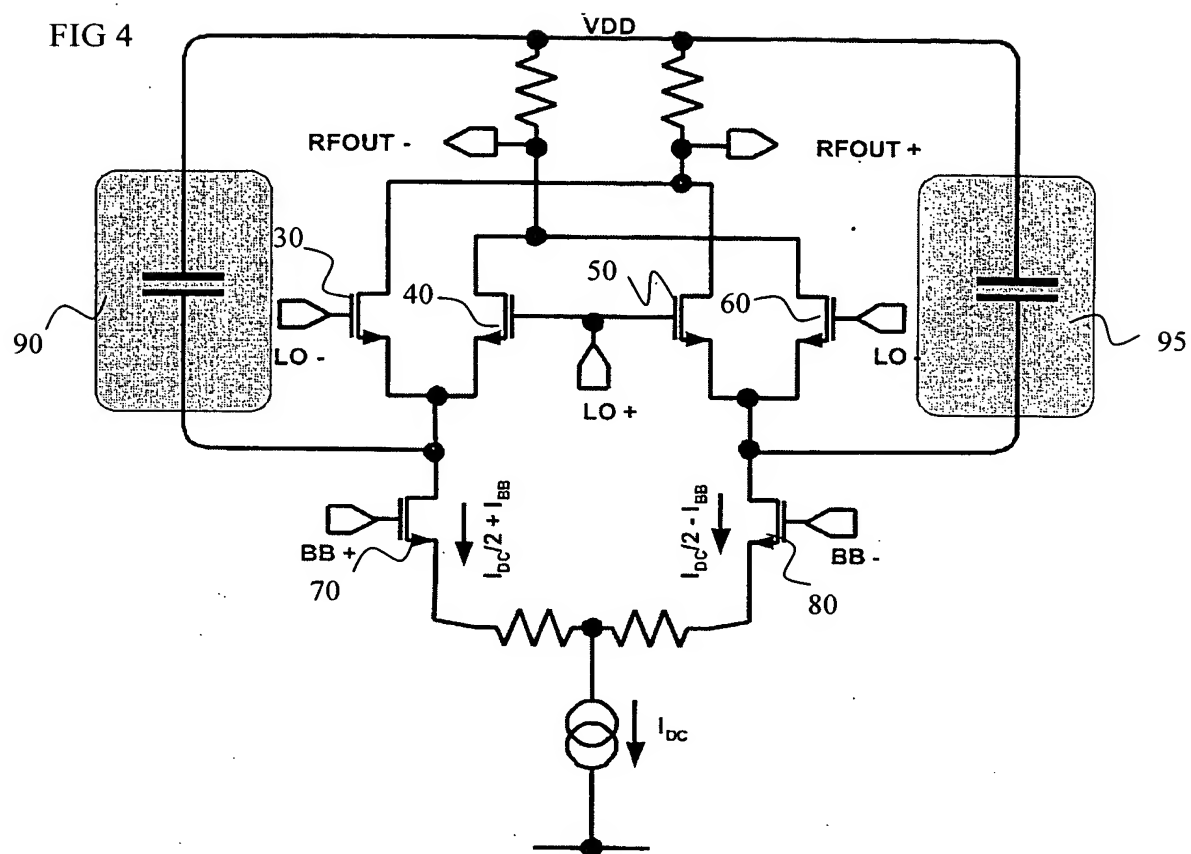


FIG 4



## Output signal

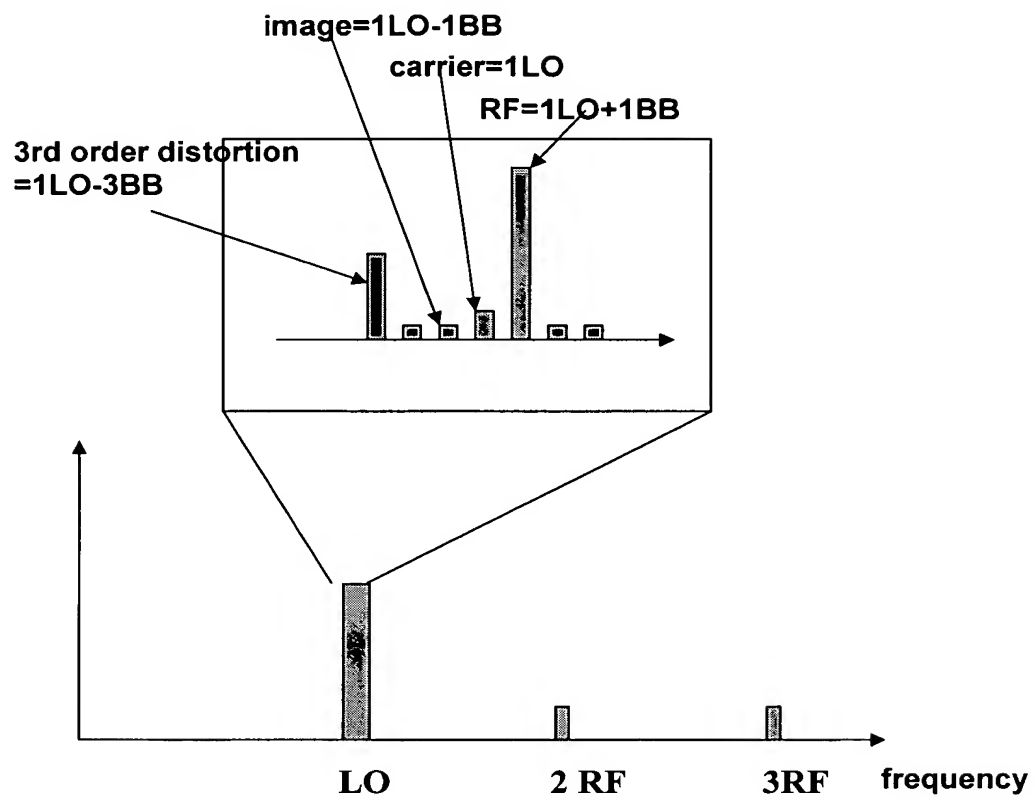


Fig. 6

At low impedance node

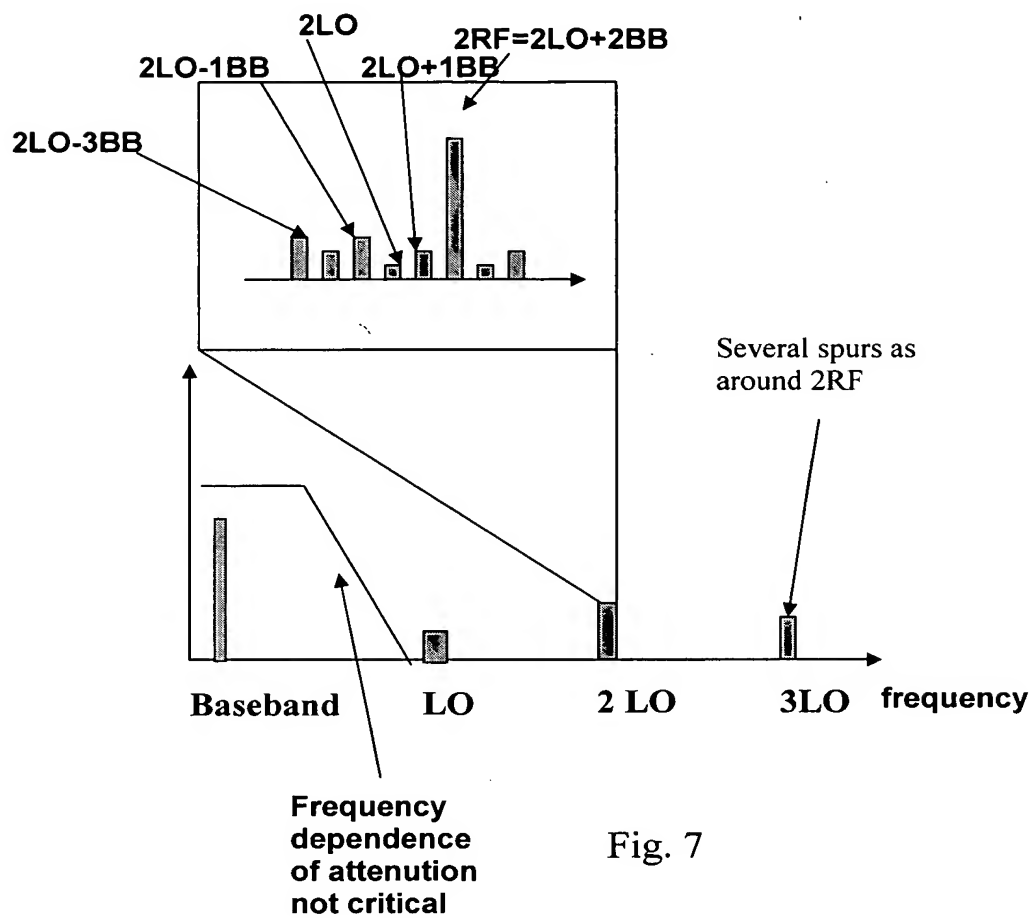


Fig. 7